



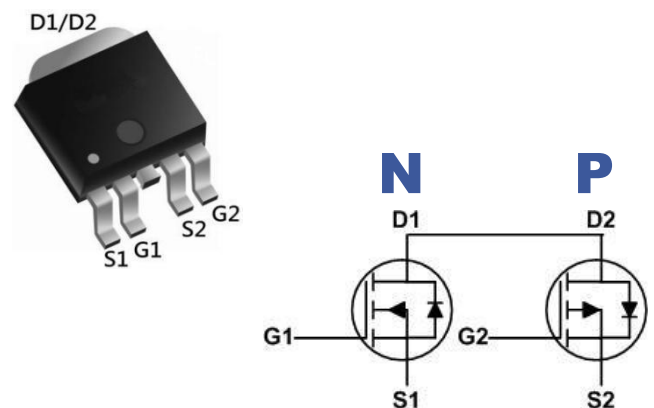
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

BVDSS	RDSON	ID
30V	15 mΩ	12A
-30V	35 mΩ	-12A

Description

The XXW3012 is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The XXW3012 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

TO252-4 Pin Configuration

Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	12	-12	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	10	-8	A
I_{DM}	Pulsed Drain Current ²	30	-30	A
EAS	Single Pulse Avalanche Energy ³	24.6	28	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	10.8	10.8	W
$P_D@T_A=25^\circ C$	Total Power Dissipation ⁴	2	2	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	32	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	6	$^\circ C/W$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V,$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note3</small>	$V_{GS}=10V, I_D=5A$	-	15	20	m Ω
		$V_{GS}=4.5V, I_D=3A$	-	21	29	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V,$ $f=1.0MHz$	-	490	-	pF
C_{oss}	Output Capacitance		-	79	-	pF
C_{rss}	Reverse Transfer Capacitance		-	61	-	pF
Q_g	Total Gate Charge	$V_{DS}=15V, I_D=5.8A,$ $V_{GS}=10V$	-	10	-	nC
Q_{gs}	Gate-Source Charge		-	1.7	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	2.5	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15V, I_D=3A,$ $V_{GS}=10V, R_{REN}=3\Omega$	-	6	-	ns
t_r	Turn-on Rise Time		-	15	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	17	-	ns
t_f	Turn-off Fall Time		-	17	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	12	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	30	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=9A$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F=5A, di/dt=100A/\mu s$	-	7	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	2	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^{\circ}\text{C}, V_{DD}=15V, V_G=10V, L=0.5mH, R_g=25\Omega, I_{AS}=6A$

3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V_{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} = -30V, V _{GS} = 0V	-	-	-1	μA
Gate-Source Leakage	I_{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Gate-Source Threshold voltage	V_{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.5	-2.5	V
Drain-Source on-State Resistance ³	R_{DS(on)}	V _{GS} = -10V, I _D = -4.1A	-	35	50	mΩ
		V _{GS} = -4.5V, I _D = -3A	-	49	75	
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	V _{GS} = 0V, V _{DS} = -15V, f = 1.0MHz	-	530	-	pF
Output Capacitance	C_{oss}		-	70	-	
Reverse Transfer Capacitance	C_{rss}		-	56	-	
Switching Characteristics⁴						
Total Gate Charge	Q_g	V _{GS} = -10V, V _{DS} = -15V, I _D = -4.1A	-	6.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.0	-	
Gate-Drain Charge	Q_{gd}		-	1.4	-	
Turn-on Delay Time	t_{d(on)}	V _{GS} = -10V, V _{DS} = -15V , R _L = 15Ω,R _{GEN} = 2.5Ω	-	14	-	ns
Rise Time	t_r		-	61	-	
Turn-off Delay time	t_{d(off)}		-	19	-	
Fall Time	t_f		-	10	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	I _S = -4.1A, V _{GS} = 0V	-	-	-1.2	V
Continuous Source Current	I_S		-	-	-12	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

N-Channel Typical Characteristics

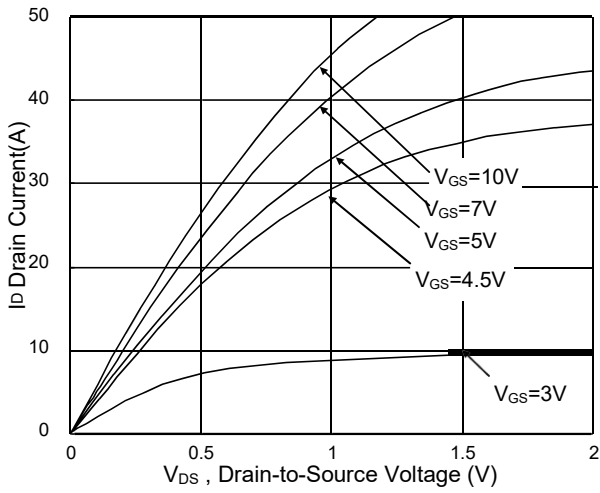


Fig.1 Typical Output Characteristics

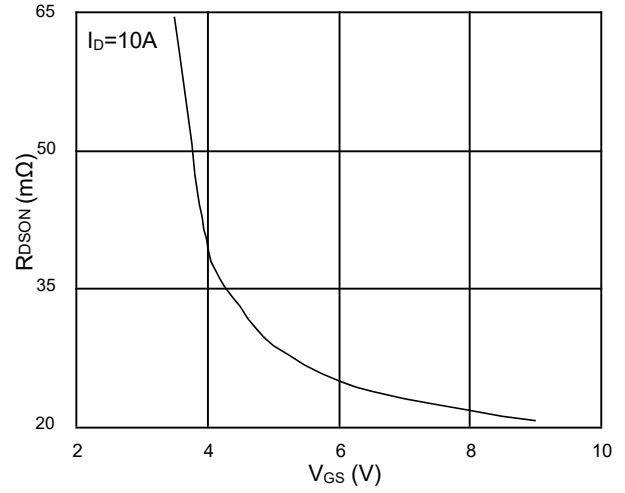


Fig.2 On-Resistance vs. Gate-Source

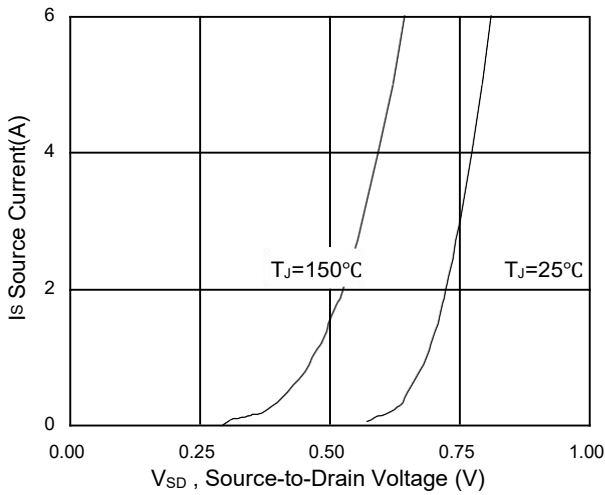


Fig.3 Forward Characteristics Of Reverse

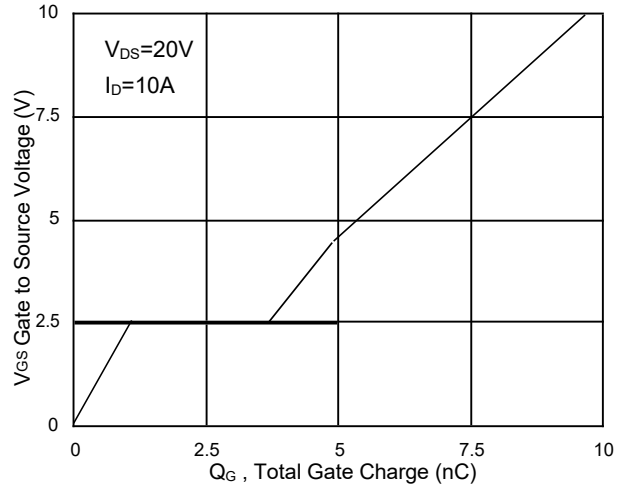


Fig.4 Gate-Charge Characteristics

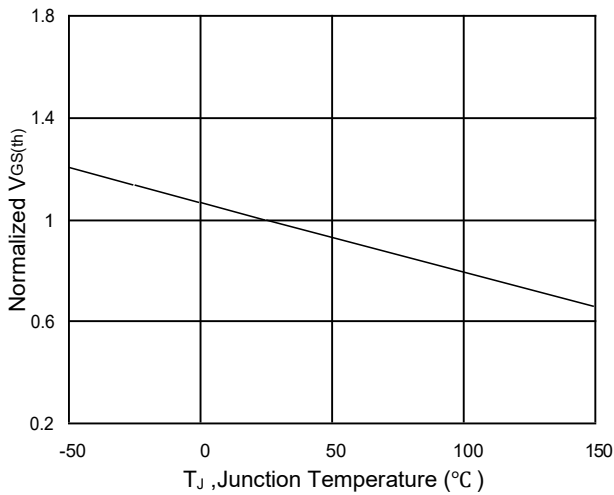


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

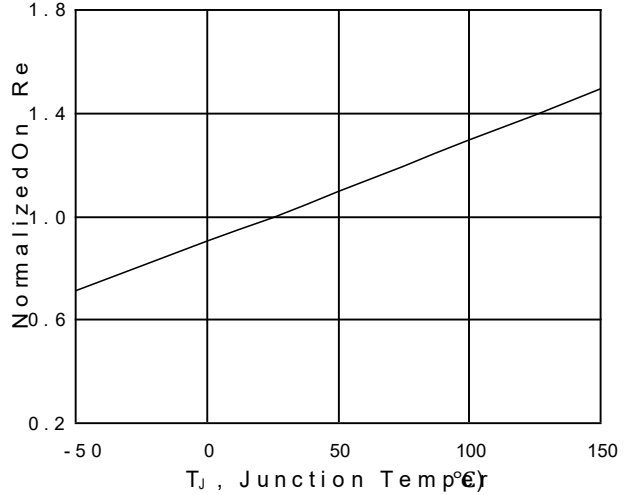
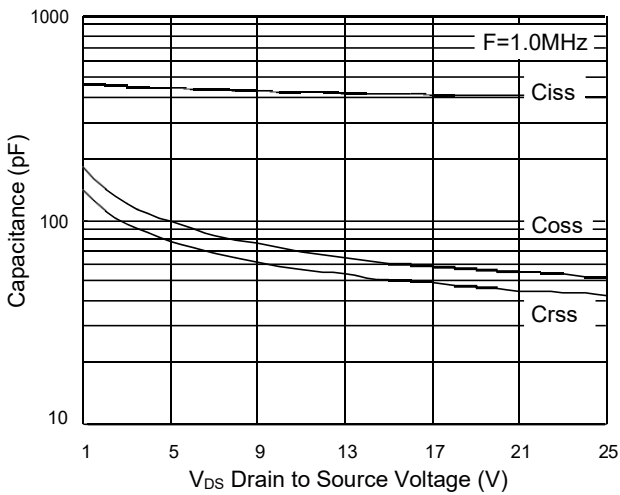
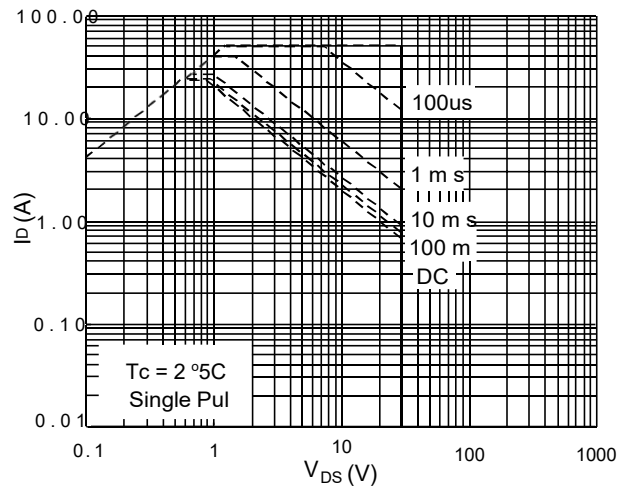
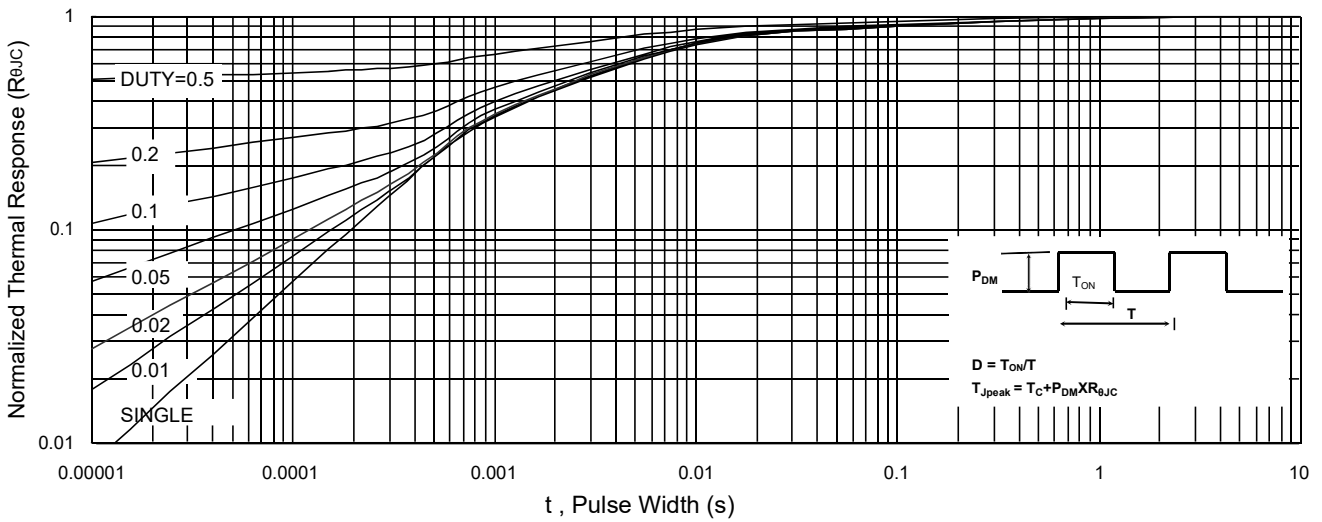
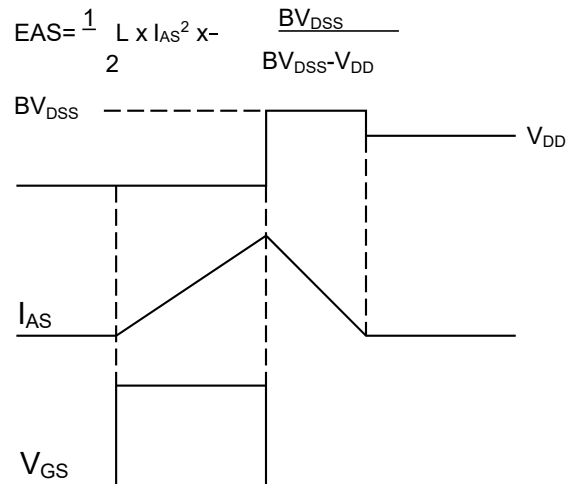
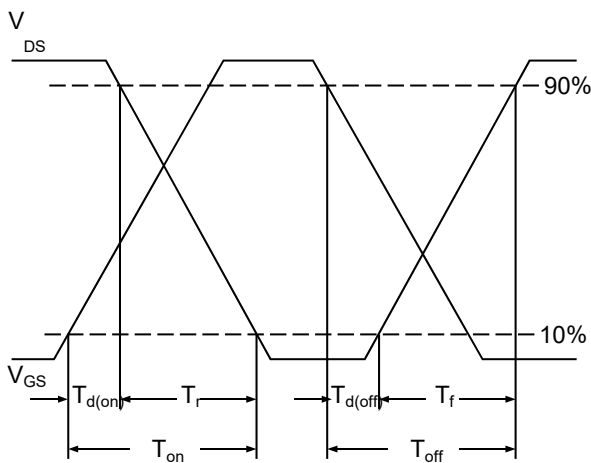


Fig.6 Normalized $R_{DS(on)}$ vs. T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance


P-Channel Typical Characteristics

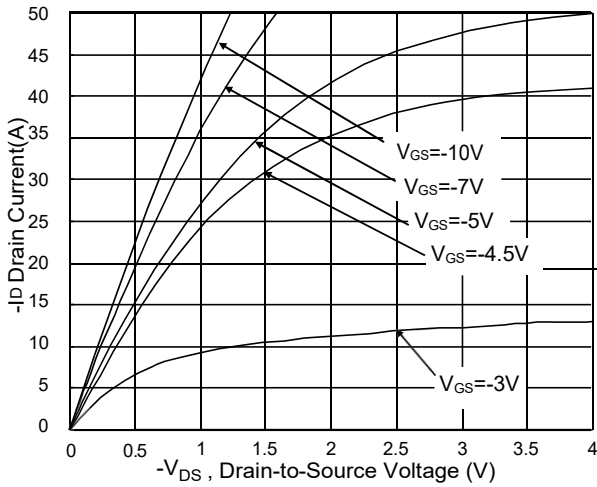


Fig.1 Typical Output Characteristics

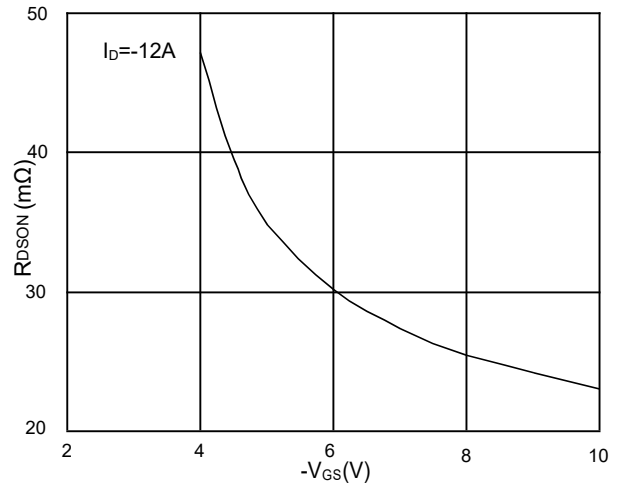


Fig.2 On-Resistance v.s Gate-Source

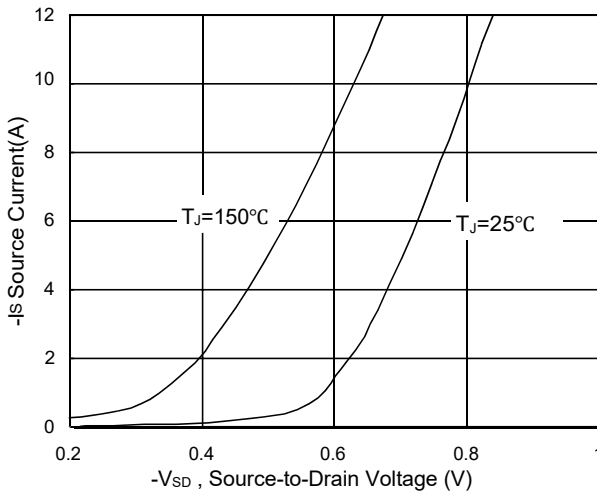


Fig.3 Forward Characteristics Of Reverse

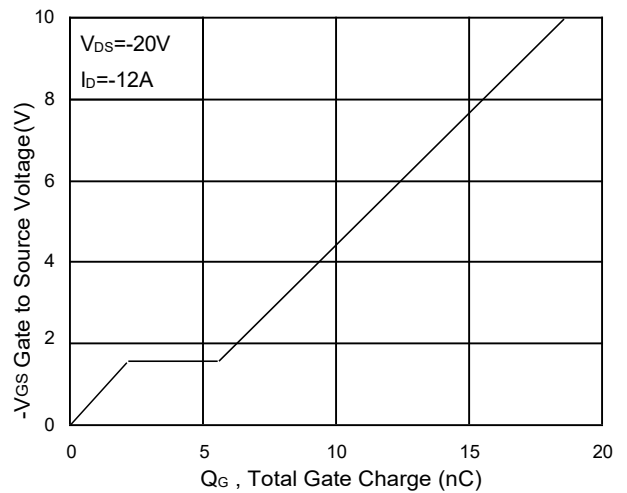


Fig.4 Gate-Charge Characteristics

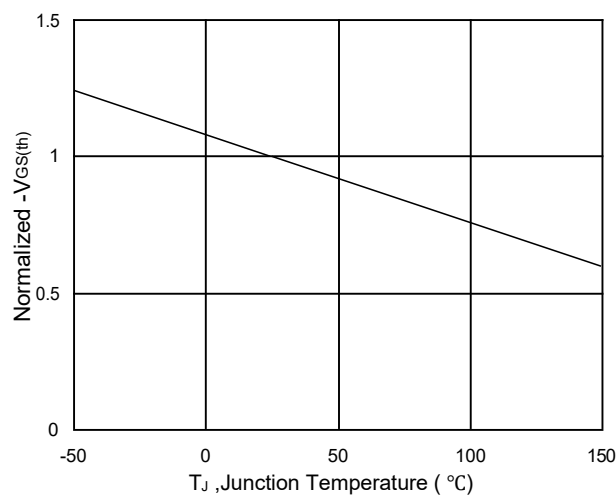


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

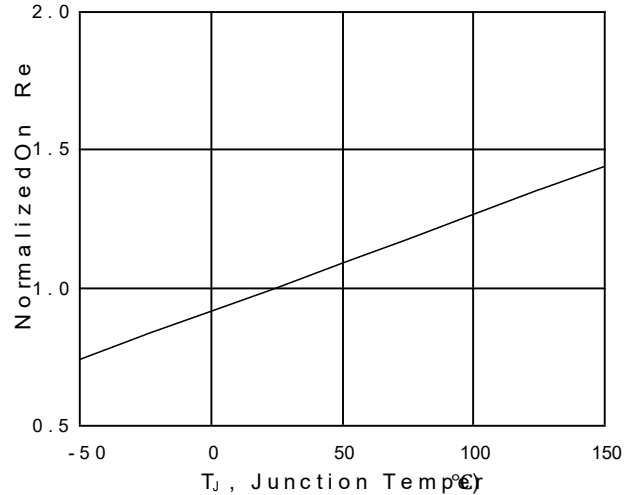
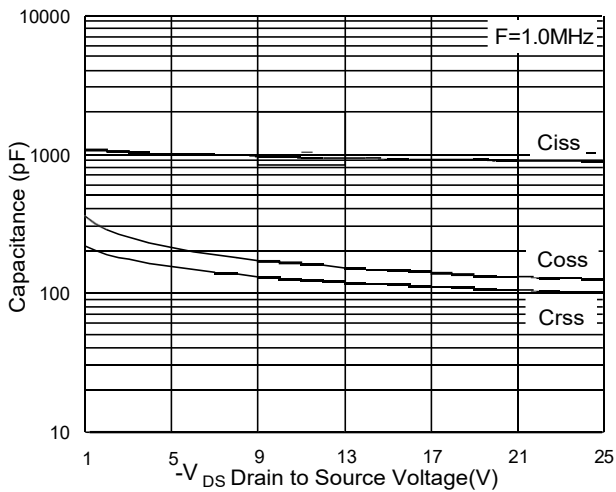
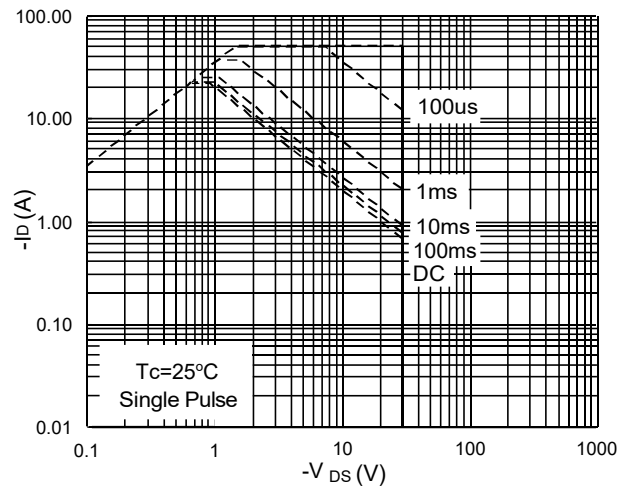
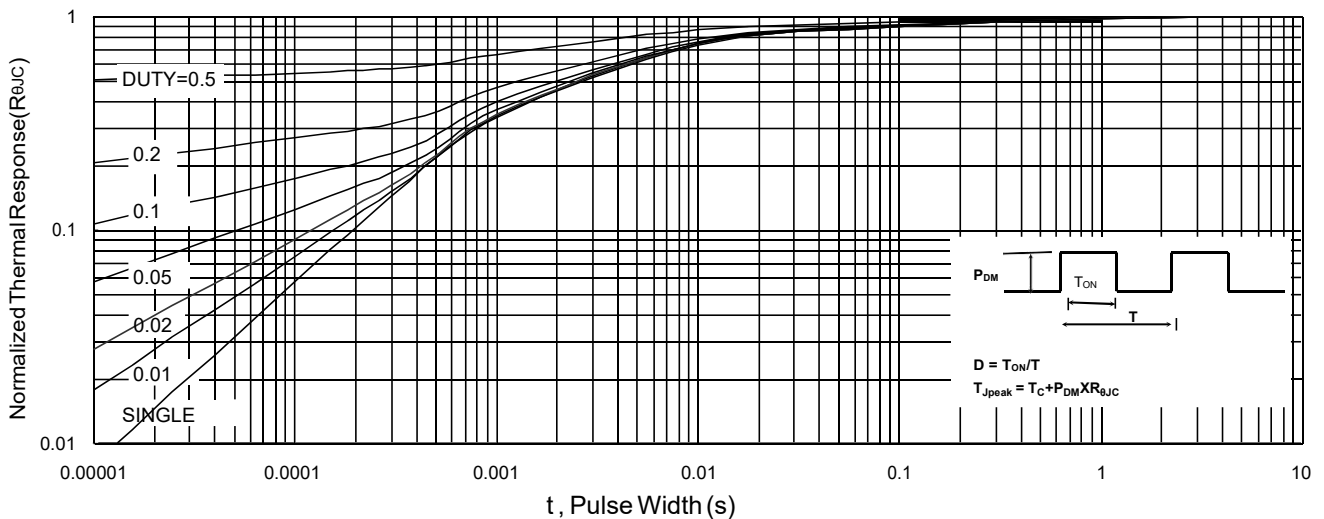
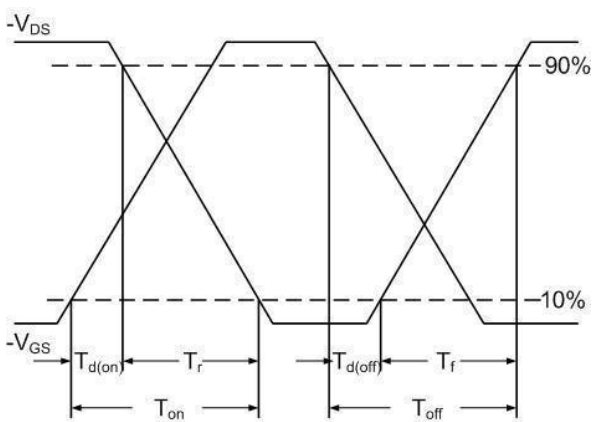
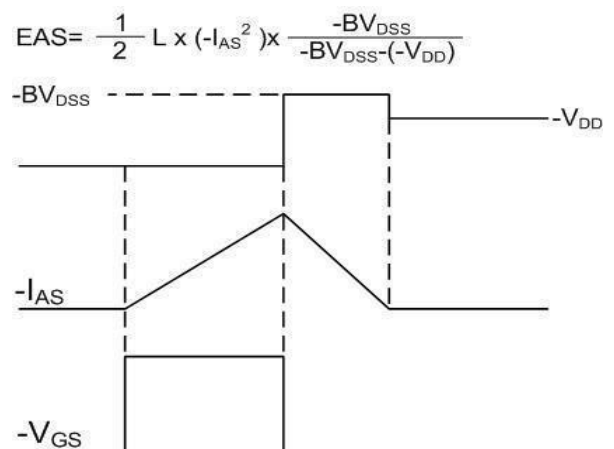
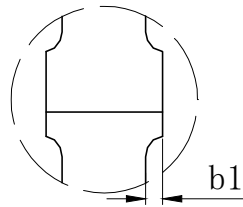
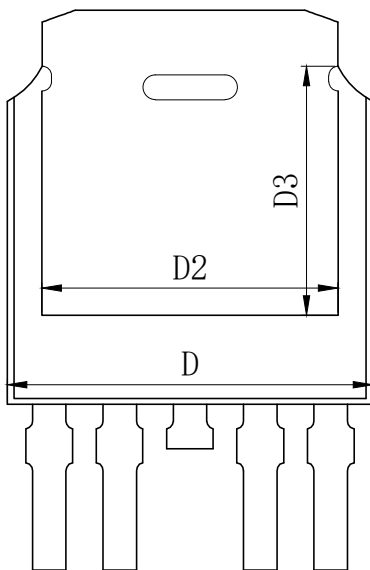
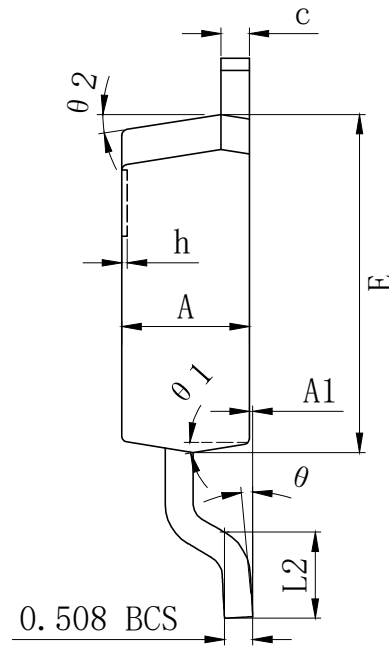
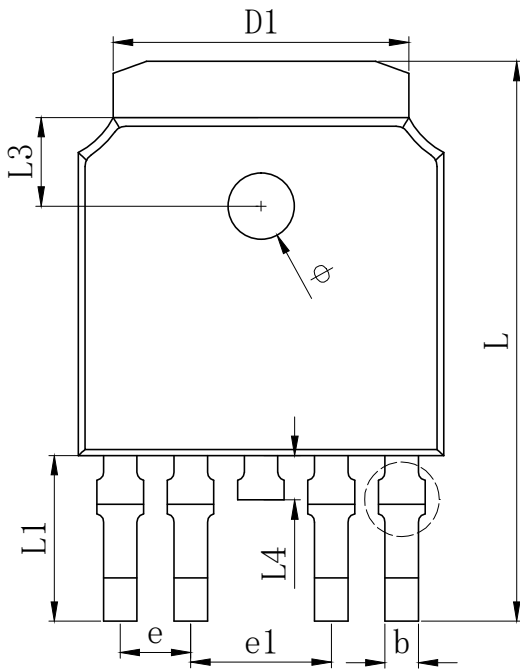


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

N-Ch and P-Ch Fast Switching MOSFETs

Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

Mechanical Dimensions for TO-252-4L


SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.550	0.600	0.650
b1	0.000		0.120
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	5.346 REF		
D3	4.490 REF		
E	6.000	6.100	6.200
e	1.270 TYP		
e1	2.540 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.988 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.700	0.800	0.900
Φ	1.100	1.200	1.300
θ	0°		8°
$\theta 1$	9° TYP		
$\theta 2$	9° TYP		